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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10030089	FILING DATE 01/14/2002	CLASS 257	SUBCLASS 368	GAU 2811	EXAMINER 2814 P. CAO
**APPLICANTS: Sakamoto Kazuhisa;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A 371 OF PCT/JP00/04715 07/13/2000 yes, pc					
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** FOREIGN APPLICATIONS VERIFIED: JAPAN 11/201875 07/15/1999 yes, pc					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials pc				ATTORNEY DOCKET NO 107400-00045	
TITLE : Semiconductor device including mos field-effect transistor					
<small>U.S. DEPT. OF COMM./PAT. & TM.-PTO-4361 (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	Total Claims <input type="checkbox"/> Print Claim for O.G. <input type="checkbox"/>
ISSUE FEE		Primary Examiner	DRAWING
Amount Due	Date Paid		Sheets Drwg. <input type="checkbox"/> Figs. Drwg. <input type="checkbox"/> Print Fig. <input type="checkbox"/>
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner
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